

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
ELIESNER ET AL.

Serial No. 10/620,151

Filing Date: JULY 15, 2003

Confirmation No. 8500

For: DIGITAL CLOCK RECOVERY CIRCUIT  
EMPLOYING FIXED CLOCK OSCILLATOR  
DRIVING FRACTIONAL DELAY LINE

Atty Docket No.:  
72206

Art Unit  
2631

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Technology Center 2600

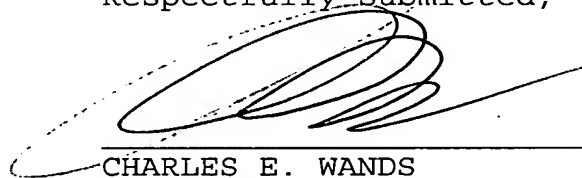
TRANSMITTAL OF FORMAL DRAWINGS

Director, U.S. Patent and Trademark Office  
Alexandria, VA 22313

Sir:

Enclosed is **one** (1) sheet of formal drawings to be  
filed in the above identified patent application.

Respectfully submitted,



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PATENT TRADEMARK OFFICE

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CERTIFICATE OF MAILING

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